PI-D Programmable Isolating DC Transmitter.

Programmable Isolating DC Voltage or DC Current Input to DC Current or DC Voltage Output Transmitter.

PI-T Thermocouple.

Features.

- Field Programmable Input and Output Ranges
- Bi-Polar Input and Output Ranges.
- Input to Output Isolation 1.6kV.
- High Accuracy 0.1%.
- Universal AC/DC Power Supply.
- Transmitter Power Supply Standard.
- Compact DIN Rail Mount Enclosure.
- Available Standard or Special Calibration.
- Low Cost Fixed Range PI-D-F Available.

Ordering Information.

PI-D-F - . . - . . - Special Fixed Calibration.

IR OR PS PI-D-F only available with ranges marked with '#'.

s.	
PI-Series Postation and the programmable Isolating Isolat	ISOSTERE LE CONTROL DE LA CONT
PI-Se isolation in the programmable	& QUALITY AWARD
	Other PI- models include:
	PI-B Bridge / Straingauge;
	PI-F Frequency;
20m A : Output 4 20m A :	PI-K Resistance;
20mA; Output 4~20mA;	PI-M Maths Computing;
tage Power Supply.	PI-N RTD Differential Pt100;
	PI-P Potentiometer;
	PI-R RTD Pt100;
	PI-S Dual Setpoint Controller;

Voltage I 0~10mV // 0~20mV // 0~50mV // 0~100mV	PUT F IR A B C D E	Current 0~200µA 0~500µA 0~1mA 0~2mA 0~5mA	IR 1 2 3 4	Voltage 0~500mV 0~1V 0~2V 0~3V	OR A B C	Current 0~1mA 0~2mA 0~5mA	OR 1 2
0~10mV 0~20mV 0~50mV 0~100mV	A B C D	0~200μA 0~500μA 0~1mA 0~2mA	1 2 3 4	0~500mV 0~1V 0~2V	A B	0~1mA 0~2mA	1 2
0~20mV 0~50mV 0~100mV	B C D	0~500μA 0~1mA 0~2mA	3 4	0~1V 0~2V	В	0~2mA	2
0~50mV 0~100mV	C D E	0~1mA 0~2mA	3 4	0~2V			
0~100mV	D E	0~2mA	4		С	0.5mA	
	Ē			03\/		U~SITIA	3
0~200mV		0~5mA		U~3 V	D	0~10mA	4
	F		5	0~4V	E	0~16mA	5
0~500mV		0~10mA	6	0~5V	F	0~20mA	6
0~1V	G	0~20mA #	7	0~6V	G	1~5mA	7
0~2V	Н	0~50mA	8	0~8V	Н	2~10mA	8
0~4V	1	0~100mA	9	0~10V #		4~20mA #	9
0~5V #	J	1~5mA	10	0~12V	J	-1~1mA	10
0~10V #	K	2~10mA	11	1~5V	K	-2~2mA	11
0~20V	L	4~20mA #	12	2~10V	L	-5~5mA	12
0~50V	М	10~50mA	13	-1~1V	M	-10~10mA	13
0~100V	N	-1~1mA	14	-2~2V	N	-20~20mA	14
0~150V	0	-10~10mA	15	-5~5V	0		
1~2V	Р	-20~20mA	16	-10~10V	Р		
	Q	20~4mA *	17	-12~12V	Q		
	R	50~10mA *	18				
	S						
-5~5V	Т						
-10~10V	U						
Special In			Z	Special Specia	l Output		Z

High Voltage Power Supply.

Ranges with '*' beside them must have the polarity of their connections reversed.

PI-D-F only available with ranges marked with '#'

POWER SUPPLY	PS
High Voltage Power Supply: 70~270Vac and 80~380Vdc	Н
Mid Voltage Power Supply: 24~80Vac and 20~90Vdc	М
Low Voltage Power Supply: 8~30Vac and 8~30Vdc	Ĺ

Note: Power supply H is field selectable for M, and M for H. Power supply L must be ordered separately.

Ordering Examples.

1/ PI-D-K-1-L 0~10V Input; 0~1mA Output; Low Voltage Power Supply. 2/ PI-D-Z-P-H-0/8V 0~8V Input; -10~10V Output; High Voltage Power Supply.

Quality Assurance Programme.

The modern technology and strict procedures of the ISO9001 Quality Assurance Programme applied during design, development, production and final inspection grant long term reliability of the instrument.

2.02-1

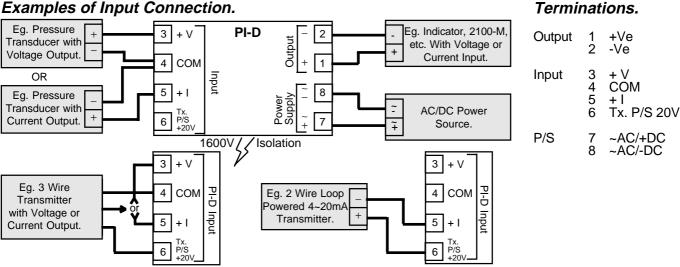
PI-D Specifications.

Note: DLD Elen	it and Outnut Danges Da	duned Defer to Ordering Information							
		duced. Refer to Ordering Information.							
Input	-Voltage	Field Programmable From 10mV to 150Vdc and Bipolar.							
		Minimum Input Resistance = $200k\Omega$.							
		Maximum Over-range = 170Vdc Continuous.							
	- Current	Field Programmable From 200μA to 100mAdc and Bipolar.							
		Input Resistance = 25Ω .							
		Maximum Over-range = 120mAdc Continuous.							
	-Transmitter P/S	20Vdc±5% Common to Input Com. (Terminal 4.)							
		Max Load = 30mA.							
		Ripple < 20mV Typical at 30mA Load.							
Output	-Voltage	Field Programmable From 500mV to ±12Vdc.							
	-	Maximum Output Drive = 10mA.							
	-Current	Field Programmable From 1mA to ±20mAdc.							
		Maximum Output Drive = $10Vdc. (500\Omega @ 20mA.)$							
Universal P/S	-Standard High (H)	70~270Vac and 80~380Vdc; 50/60Hz; 4VA.							
	-Standard Mid (M)	24~80Vac and 20~90Vdc; 50/60Hz; 4VA.							
	-Low Voltage (L)	8~30Vac and 8~30Vdc; 50/60Hz; 4VA.							
	-Circuit Sensitivity	<±0.001%/V FSO Typical.							
Accurate to		<±0.1% FSO Typical.							
Linearity & Repea	atability	<±0.1% FSO Typical.							
Ambient Drift	•	<±0.01%/C FSÔ Typical.							
Noise Immunity		125dB CMRR Average. (1.6kV Peak Limit.)							
R.F. Immunity		<1% Effect FSO Typical.							
Isolation Voltage		1.6kVac/dc Peak Input to Output for 60sec.							
Response Time		200msec Typical. (From 10 to 90% 50msec Typical.)							
Operating Tempe	erature	0~70C.							
Storage Tempera		-20~80C.							
Operating Humid		90%RH max. Non-Condensing.							
Construction	•	Socket Plug-In Type with Barrier Terminals.							
Nata 4 On a Constitution	a la a carda de Otana da dal Otalia de Car	a Half contains a disconsider consultation							

Note 1. Specifications based on Standard Calibration Unit, unless otherwise specified.

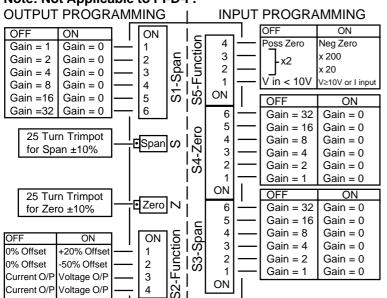
Note 2. Due to ongoing research and development, designs, specifications, and documentation are subject to change without notification. No liability will be accepted for errors, omissions or amendments to this specification.





Plan View of PI-D Adjustments.

Note: Not Applicable to PI-D-F.



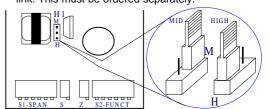
PI-D H1 Power Supply Link Settings.



WARNING: High Voltages Maybe Present. Only adjust link with power disconnected.

Power Supply Link Settings											
H1	H1 Power Supply Voltage Range										
Н	Link for High: 70~270Vac / 80~380Vdc										
М	Link for Mid: 24~80Vac / 20~90Vdc										

- 1/ H1 is approx 4cm (11/2") behind the 'S' trimpot.
- 2/ Exceeding voltage ranges may damage the unit.
- 3/ Ensure the enclosure label is correctly labelled for the link position.
- 4/ Adjust H1 jumper with a pair of needle nose pliers.
- 5/ Low Voltage Power Supply version is fixed, and has no link. This must be ordered separately.



PI-D Input Programming.

Note: Not Applicable to PI-D-F.

Always set **OUTPUT** range first, then INPUT range. If the input range is not listed in the programming table, use the following formulae to work out the Zero and Span DIP switch settings for gain.

Span Gain = $\frac{24}{\text{Pregain x (Signal High - Signal Low)}}$

Zero Gain = 10 x Pregain x Signal Low.

If Zero is 1/ Positive, put S5-4 OFF.

2/ Negative, put S5-4 ON.

	EFFECTIVE INPUT RANGE (ie Signal High - Signal Low)	S5-1	S5-2	S5-3	PREGAIN
	10mV <= Range <= 20mV	0	0	0	200
VOLTAGE INPUT	20mV < Range <= 200mV	0	0	1	20
[달립	200mV < Range <= 2V	0	1	1	2
│ ♀⋛	2V < Range <= 20V	1	0	1	0.2
Ш	20V < Range <= 150V	1	1	1	0.02
CURRENT INPUT	200μA <= Range <= 800μA	1	0	0	5000
LE 링	800μA < Range <= 8mA	1	0	1	500
[5 €	8mA < Range <= 50mA	1	1	1	50

So if a gain value of 28 is required, put DIP switch No's 3, 4, 5 (ie, gains of 4 + 8 + 16 = 28) OFF and all the other DIP switches ON.

DIP switches and trimpots are accessed by removing the small rectangular lid on the top of the PI-D enclosure.

Gain Value	1	2	4	8	16	32
DIP Switch No.	1	2	3	4	5	6

Notes: (a) Enter ranges with their exponential value: Eg. Enter 20mA as 20 x 10^{-3} ; Enter 100mV as 100 x 10^{-3} .

(b) Use the same pregain value in both the Span and Zero gain formulae.

(c) Enter the Zero or Špan gain value into the appropriate Zero or Span DIP switch.

(d) If your GAIN ZERO exceeds 63, then your input range will need to be factory calibrated.

PI-D Input Range Programming Table.

Note: Not Applicable to PI-D-F.

Notes: 1/ Switch status 1 = ON, 0 = OFF, X = DON'T CARE.

 $2/\,$ Input ranges with $^{\mbox{\tiny I*'}}$ beside them reverse the polarity of the input connections.

3/ Input ranges with '#' beside them require more adjustment with the Zero and Span trimpots.

Input Range		;	S3-S	PAN	1			ERC	S5-FUNCTION							
input Kange	1	2	3	4	5	6	1	2	3	4	5	6	1	2	3	4
0~10mV	1	1	0	0	1	1	1	1	1	1	1	1	0	0	0	Х
0~20mV	1	0	0	1	1	1	1	1	1	1	1	1	0	0	0	Х
0~50mV	1	1	1	0	0	1	1	1	1	1	1	1	0	0	1	Х
0~100mV	1	1	0	0	1	1	1	1	1	1	1	1	0	0	1	Х
0~200mV	1	0	0	1	1	1	1	1	1	1	1	1	0	0	1	Х
0~500mV	1	1	1	0	0	1	1	1	1	1	1	1	0	1	1	Х
0~1V	1	1	0	0	1	1	1	1	1	1	1	1	0	1	1	Х
0~2V	1	0	0	1	1	1	1	1	1	1	1	1	0	1	1	Х
0~4V	1	0	0	0	0	1	1	1	1	1	1	1	1	0	1	Х
0~5V	1	1	1	0	0	1	1	1	1	1	1	1	1	0	1	Х
0~10V	1	1	0	0	1	1	1	1	1	1	1	1	1	0	1	Х
0~20V	1	0	0	1	1	1	1	1	1	1	1	1	1	0	1	Х
0~50V	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	Х
0~100V	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	Х
0~150V	1	1	1	0	1	1	1	1	1	1	1	1	1	1	1	Х
1~5V	1	0	0	0	0	1	1	0	1	1	1	1	1	0	1	0
2~10V	0	0	0	0	1	1	1	1	0	1	1	1	1	0	1	0
-1~1V	1	0	0	1	1	1	1	1	0	1	0	1	0	1	1	1
-5~5V	1	1	0	0	1	1	1	0	1	0	1	1	1	0	1	1
-10~10V	1	0	0	1	1	1	1	1	0	1	0	1	1	0	1	1
0~200µA	1	1	1	0	0	1	1	1	1	1	1	1	1	0	0	Х
0~500µA#	1	0	1	0	1	1	1	1	1	1	1	1	1	0	0	Х
0~1mA	1	1	1	1	0	0	1	1	1	1	1	1	1	0	1	Х
0~2mA	1	1	1	0	0	1	1	1	1	1	1	1	1	0	1	Х
0~5mA#	_ 1_	0	1	0	1	1	1	1	1	1	1	1	1	0	1	Х
0~10mA	1	1	1	1	0	0	1	1	1	1	1	1	1	1	1	Х
0~20mA	1	1	1	0	0	1	1	1	1	1	1	1	1	1	1	Х
0~40mA	1	1	0	0	1	1	1	1	1	1	1	1	1	1	1	Х
0~50mA#	1	0	1	0	1	1	1	1	1	1	1	1	1	1	1	Х
1~5mA	1	1	0	0	1	1	0	1	0	1	1	1	1	0	1	0
2~10mA	1	0	0	1	1	1	1	0	1	0	1	1	1	0	1	0
4~20mA	1	0	0	0	0	1	1	0	1	1	1	1	1	1	1	0
10~50mA	1	1	0	0	1	1	0	1	0	1	1	1	1	1	1	0
-1~1mA	1	1	1	0	0	1	0	1	0	1	1	1	1	0	1	1
-10~10mA	1	1	1	0	0	1	0	1	0	1	1	1	1	1	1	1
-20~20mA	1	1	0	0	1	1	1	0	1	0	1	1	1	1	1	1
* 20~4mA	1	0	0	0	0	1	1	0	1	0	1	1	1	1	1	1
* 50~10mA	1	1	0	0	1	1	0	1	1	0	0	1	1	1	1	1

PI-D Output Range Programming Table.

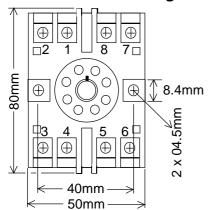
Note: Not Applicable to PI-D-F.

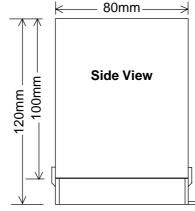
Notes:

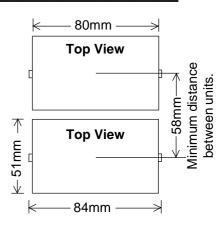
Switch status 1 = ON = OFFOutput ranges with '*' beside them reverse the polarity of the output connections. 2/

Output		S	1-8	PA	N		S2	S2-Function		Output		S	1-8	PA	N		S2-Function					
Range (V)	1	2	3	4	5	6	1	2	3	4	Range (I)	1	2	3	4	5	6	1	2	3	4	
0~500mV	0	1	1	1	1	1	0	0	1	1	0~1mA	0	1	1	1	1	1	0	0	0	0	
0~1V	1	0	1	1	1	1	0	0	1	1	0~2mA	1	0	1	1	1	1	0	0	0	0	
0~2V	1	1	0	1	1	1	0	0	1	1	0~5mA	0	1	0	1	1	1	0	0	0	0	
0~3V	1	0	0	1	1	1	0	0	1	1	0~10mA	1	0	1	0	1	1	0	0	0	0	
0~4V	1	1	1	0	1	1	0	0	1	1	0~16mA	1	1	1	1	0	1	0	0	0	0	
0~5V	1	0	1	0	1	1	0	0	1	1	0~20mA	1	1	0	1	0	1	0	0	0	0	
0~6V	1	1	0	0	1	1	0	0	1	1	1~5mA	1	1	0	1	1	1	1	0	0	0	
0~8V	1	1	1	1	0	1	0	0	1	1	2~10mA	1	1	1	0	1	1	1	0	0	0	
0~10V	1	1	0	1	0	1	0	0	1	1	4~20mA	1	1	1	1	0	1	1	0	0	0	
0~12V	1	1	1	0	0	1	0	0	1	1	-1~1mA	1	0	1	1	1	1	0	1	0	0	
1~5V	1	1	1	0	1	1	1	0	1	1	-2~2mA	1	1	0	1	1	1	0	1	0	0	
2~10V	1	1	1	1	0	1	1	0	1	1	-5~5mA	1	0	1	0	1	1	0	1	0	0	
-1~1V	1	1	0	1	1	1	0	1	1	1	-10~10mA	1	1	0	1	0	1	0	1	0	0	
-2~2V	1	1	1	0	1	1	0	1	1	1	-20~20mA	1	1	1	0	1	0	0	1	0	0	
-5~5V	1	1	0	1	0	1	0	1	1	1	0~-10mA *	1	0	1	0	1	1	0	0	0	0	
-10~10V	1	1	1	0	1	0	0	1	1	1	0~-20mA *	1	1	0	1	0	1	0	0	0	0	
-12~12V	1	1	1	1	0	0	0	1	1	1												
0~-5V *	1	0	1	0	1	1	0	0	1	1												
0~-10V *	1	1	0	1	0	1	0	0	1	1												

Dimensions and Mounting.







8PFA Octal Termination Base

The Proper Installation & Maintenance of PI-D.

MOUNTING.

- Mount in a clean environment in an electrical cabinet on 35mm, symetrical, mounting rail.
- (1) (2) (3) (4) Do not subject to vibration or excess temperature or humidity variations.
- Avoid mounting in cabinets with power control equipment.
- To maintain compliance with the EMC Directives the PI-D is to be mounted in a fully enclosed steel cabinet. The cabinet must be properly earthed, with appropriate input / output entry points, filtering and cabling.

WIRING.

- A readily accessible disconnect device and overcurrent device must be incorporated in the the power supply wiring.
- (1) (2) (3) (4) All cables should be good quality overall screened INSTRUMENTATION CABLE with the screen earthed at one end only.
- Signal cables should be laid a minimum distance of 300mm from any power cables.
- For 2 wire current loops, 2 wire voltage signals or 2 wire current signals, Austral Standard Cables B5102ES is recommended. For 3 wire transmitters Austral Standard Cables B5103ES is recommended.
- It is recommended that you do not ground current loops and use power supplies with ungrounded outputs.
- Lightning arrestors should be used when there is a danger from this source.
- (6) (7) Refer to diagrams for connection information.

COMMISSIONING.

- Once all the above conditions have been carried out and the wiring checked apply power to the PI-D loop and allow five (1) minutes for it to stabilize.
- Take a low (approx 10%) and high (approx 90%) reading of the variable being measured by the transducer supplying the (2)signal to the PI-D, and ensure that this agrees with the level being indicated by the PLC or indicator, etc, that the PI-D is connected into. Adjust for any difference using the Zero and Span trimpots in the top of the PI-D enclosure with a small screw driver until the two levels agree. (Clockwise to increase the output reading and anti-clockwise to decrease the output reading.)

MAINTENANCE.

- Repeat (2) of Commissioning.
- Do it regularly at least once every 12 months.

INSTRUMENTS LTD.

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